

FIG. 1

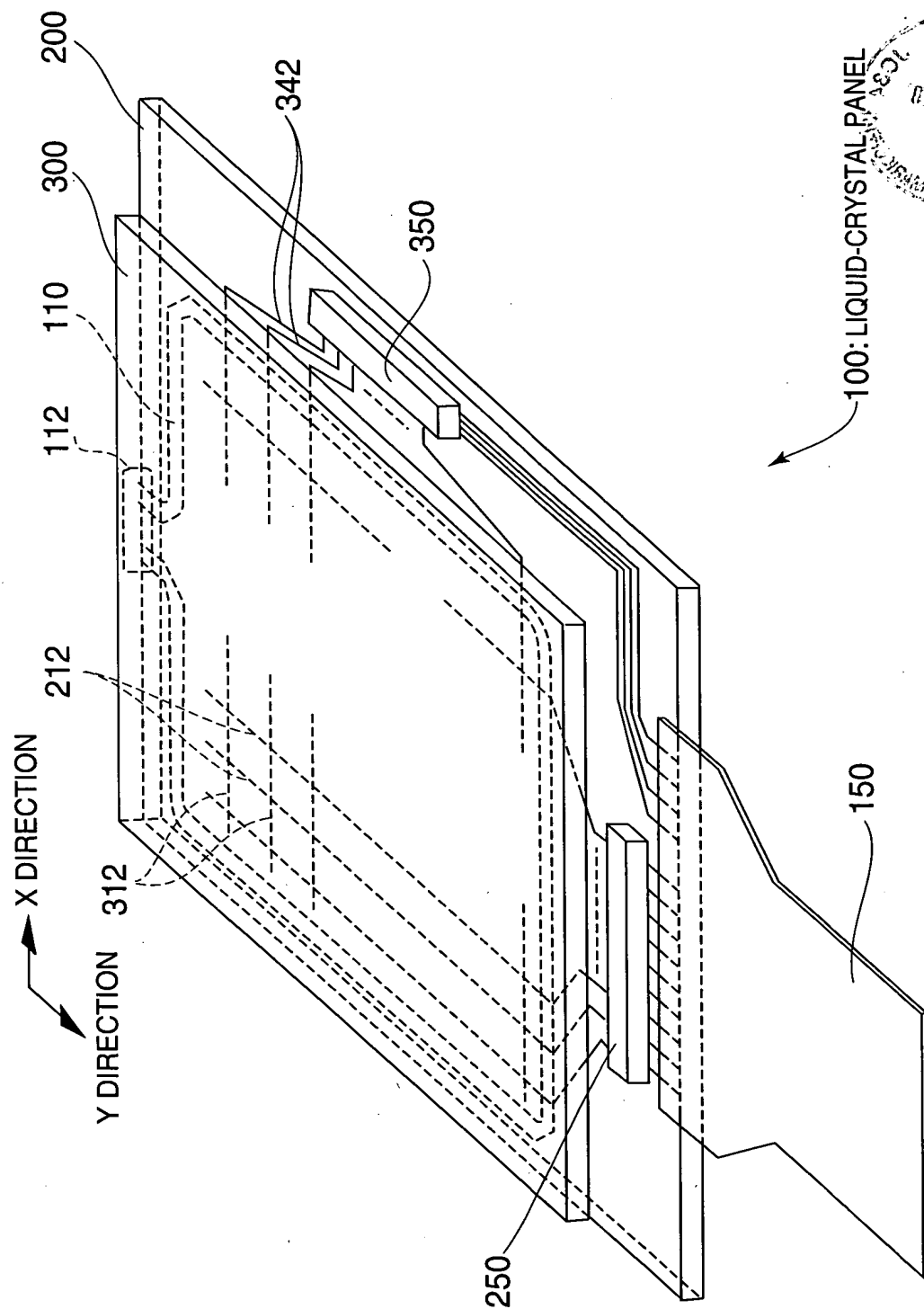
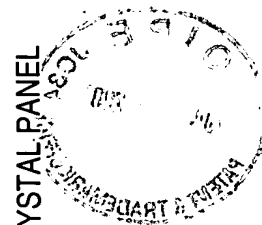
[illegible]

FIG. 2



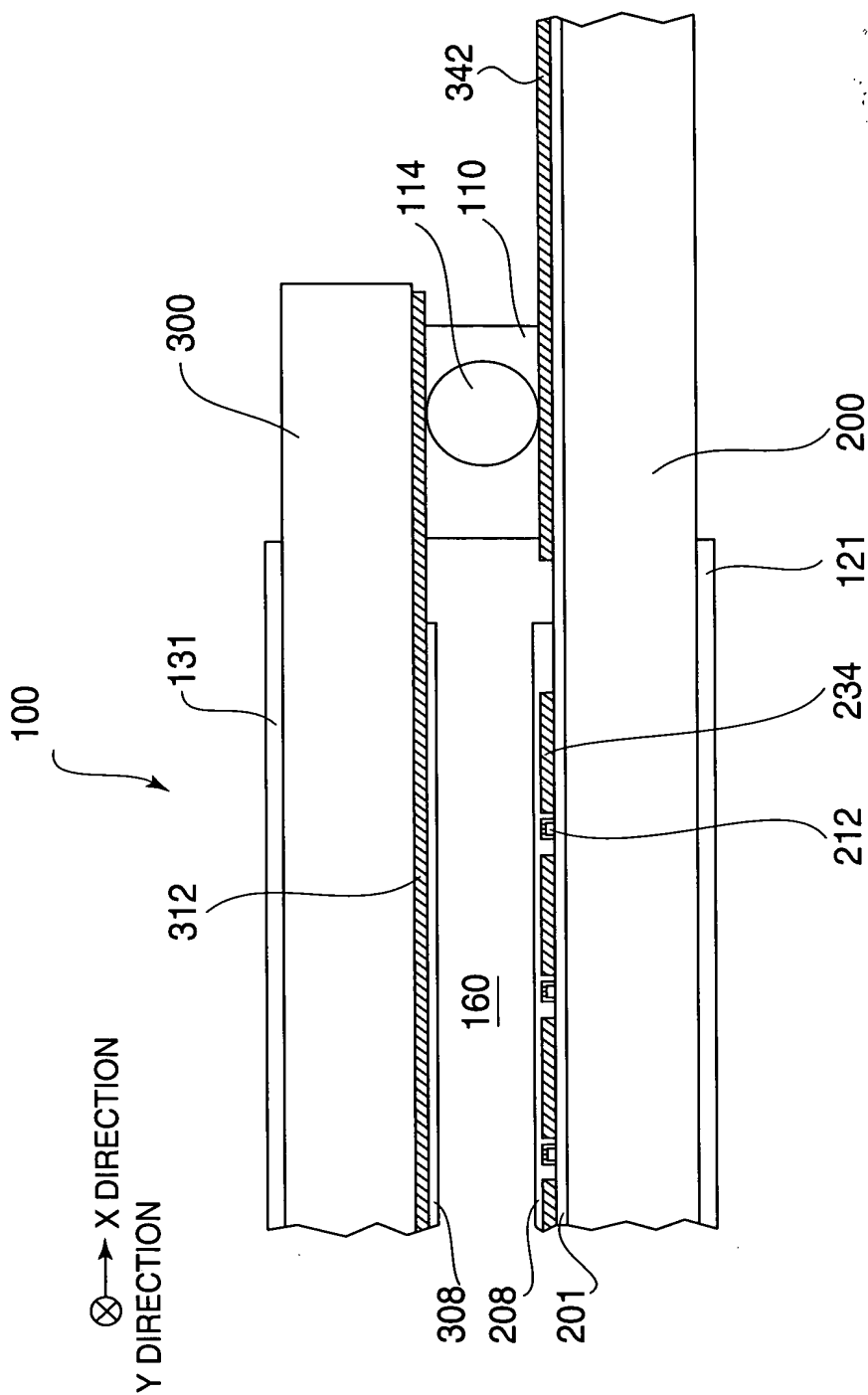


FIG. 3

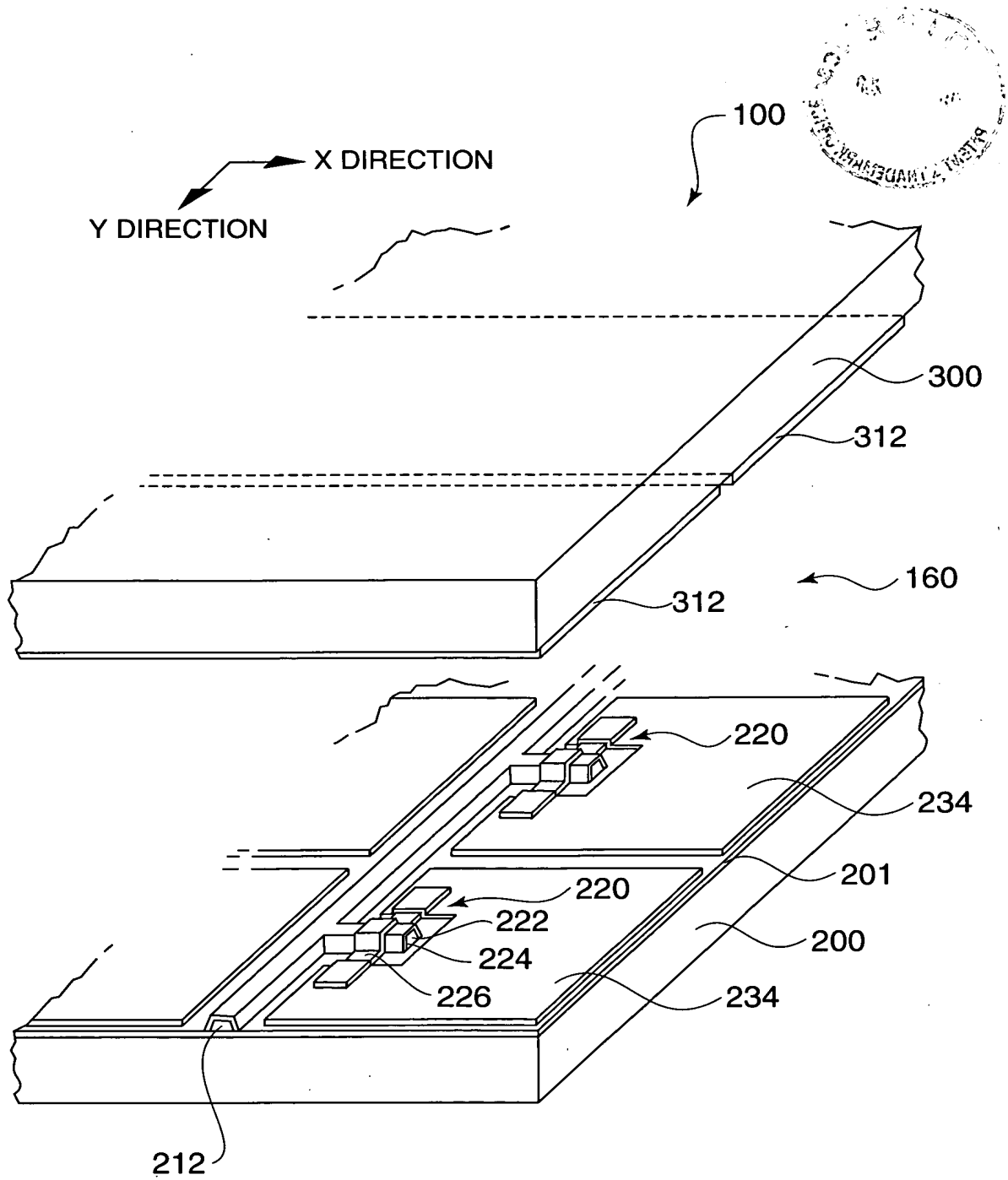


FIG. 4

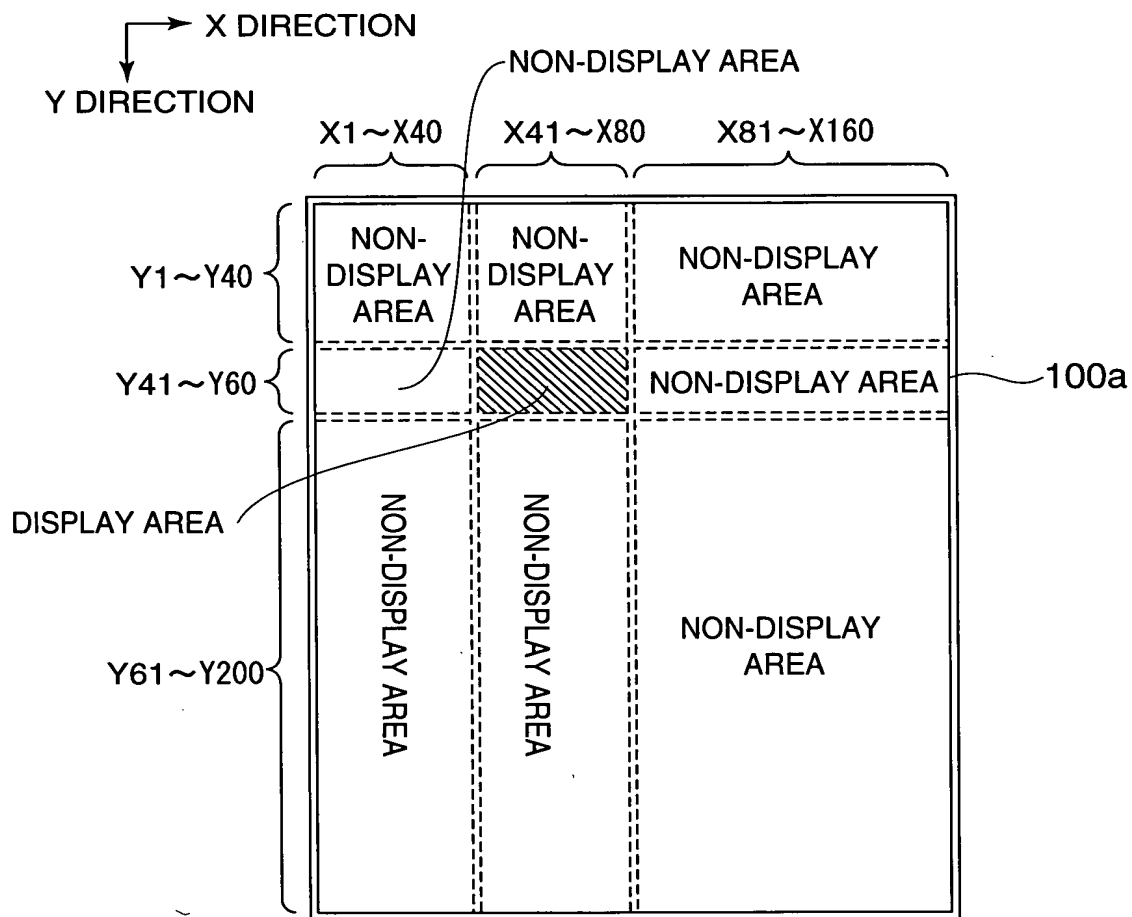


FIG. 5

0921583 111301

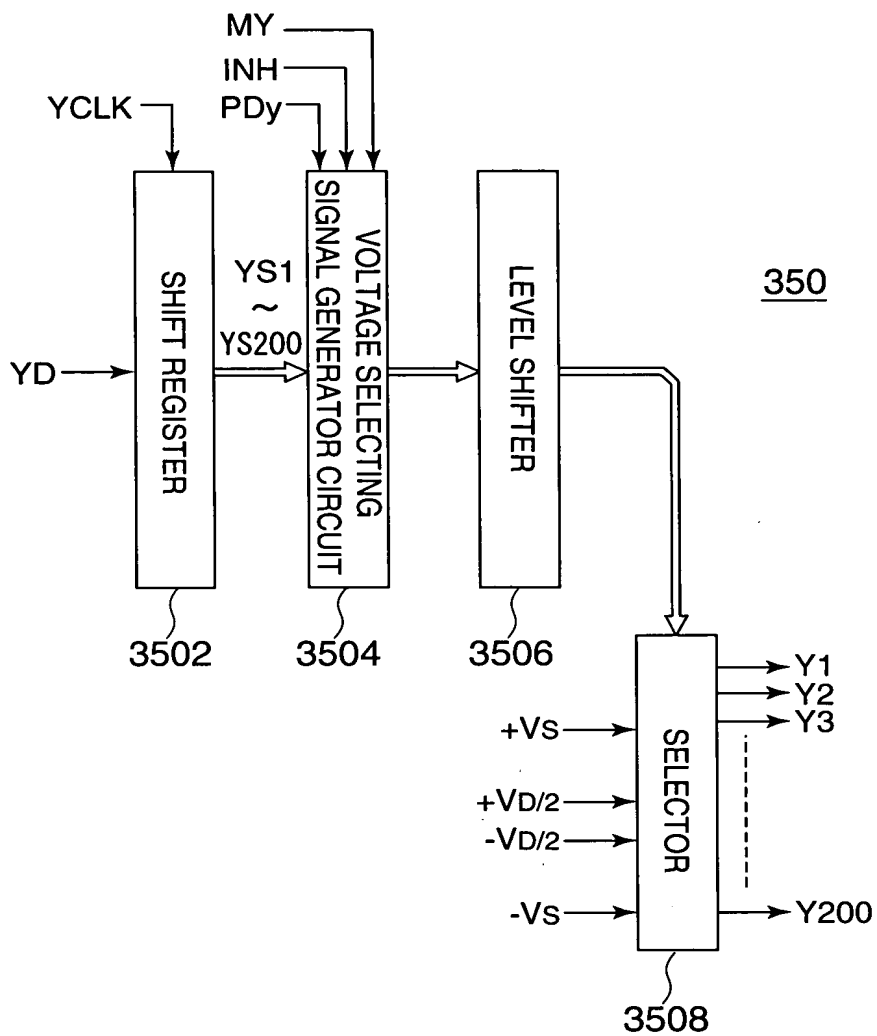


FIG. 6

09921583.1.1.30.1

<Y SIDE: STANDARD MODE>

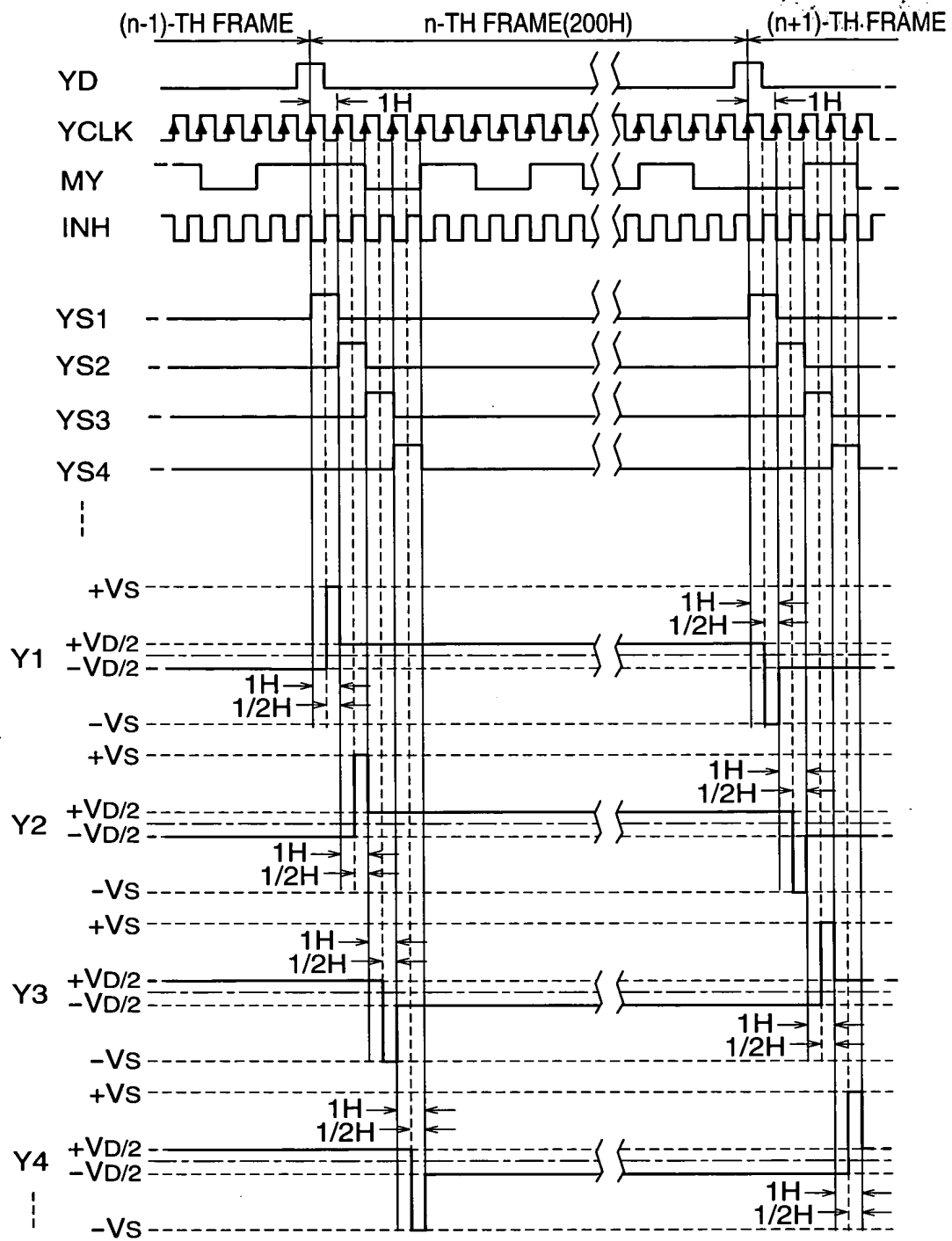


FIG. 7

090153-11301

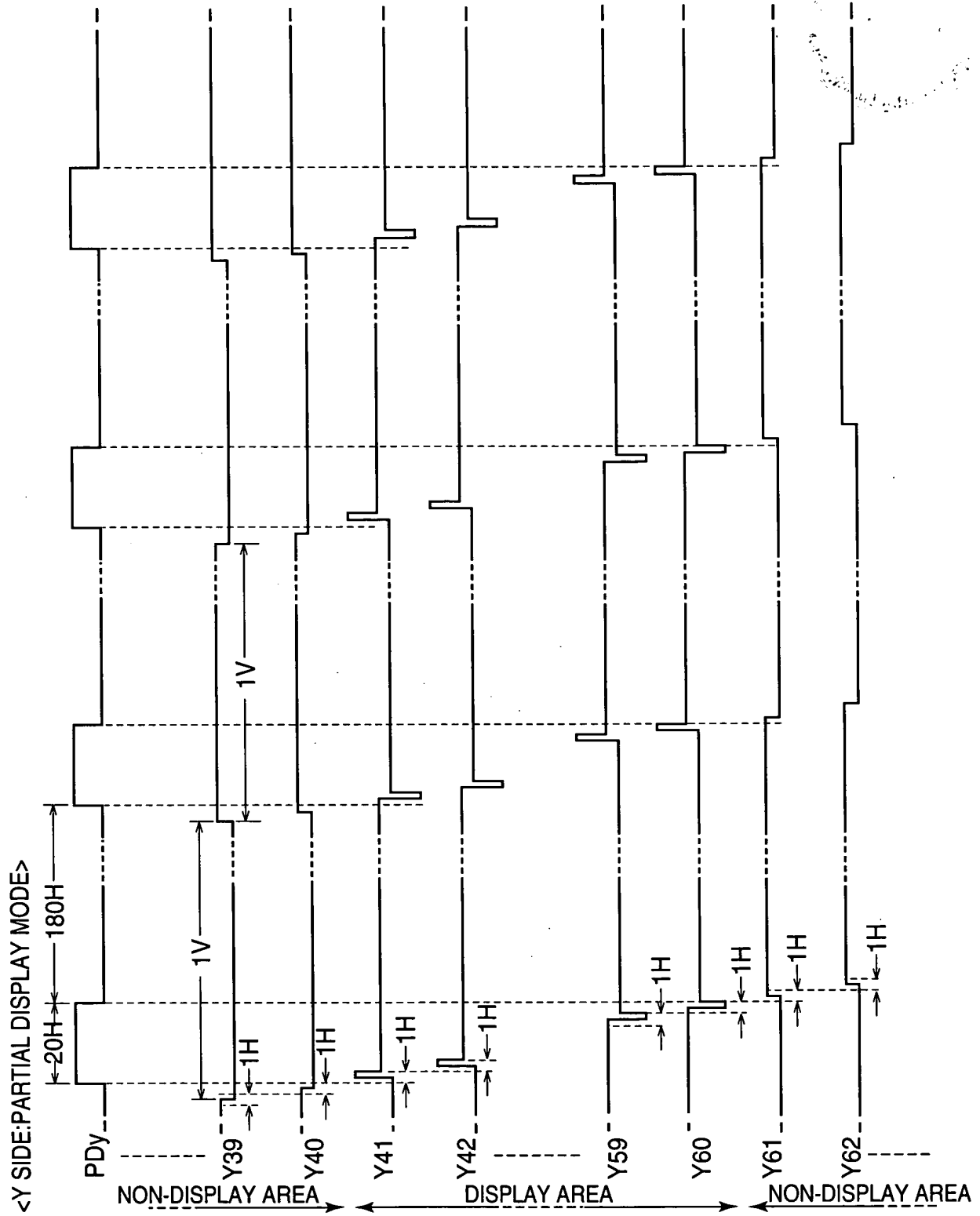
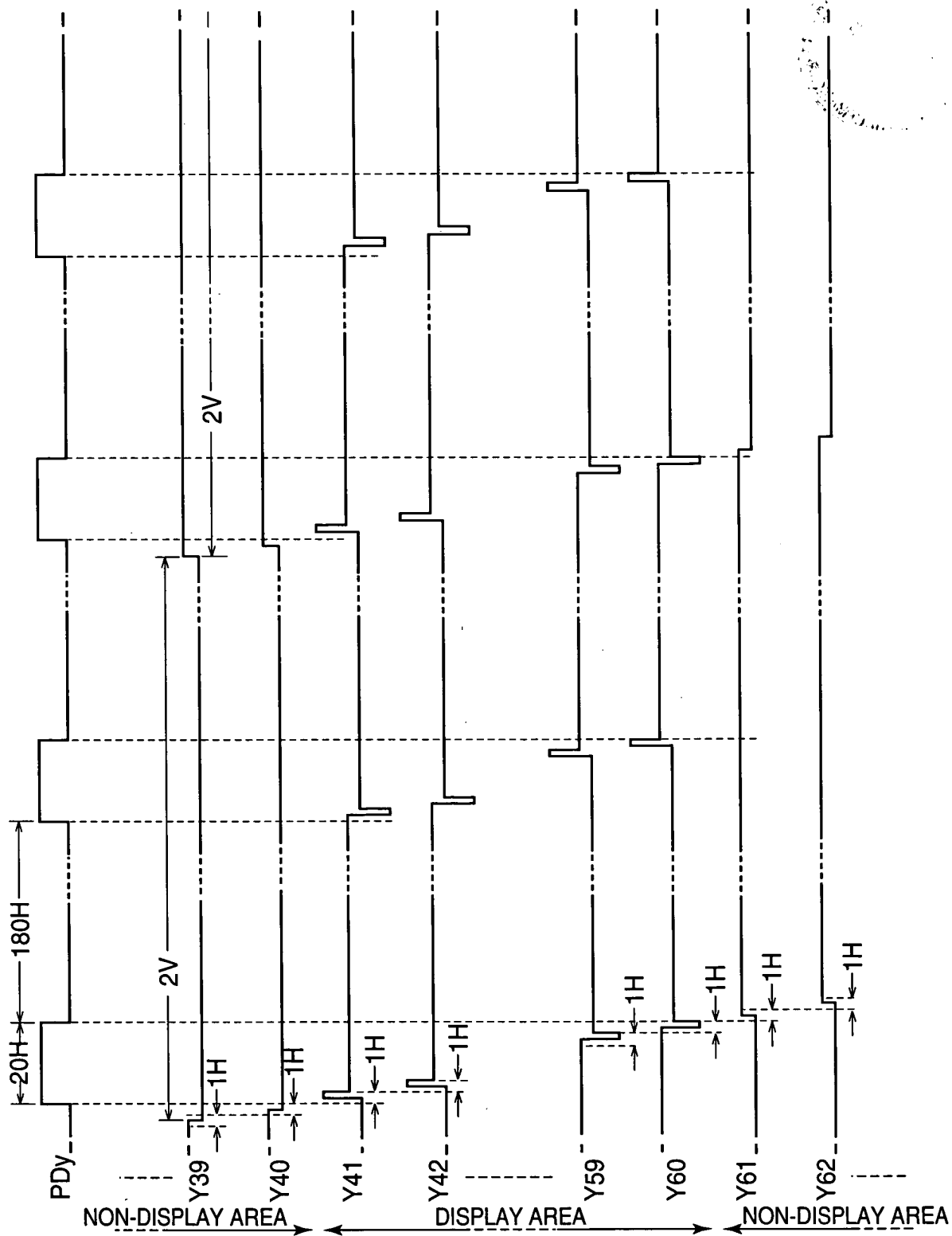


FIG. 8







250

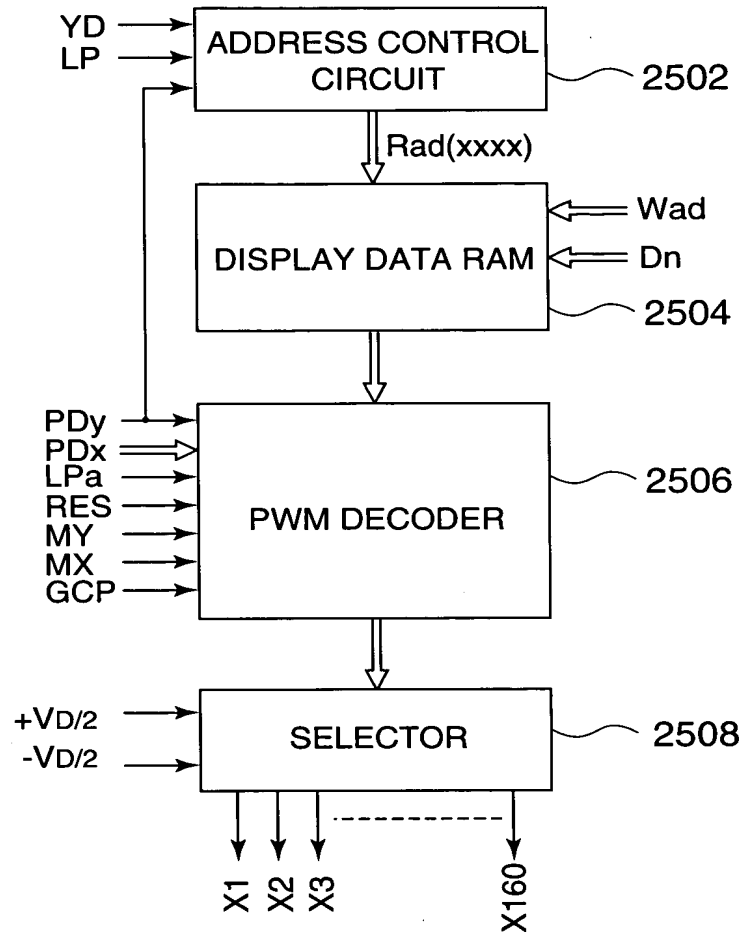


FIG. 10

FIG. 10

<X SIDE: PARTIAL DISPLAY MODE>

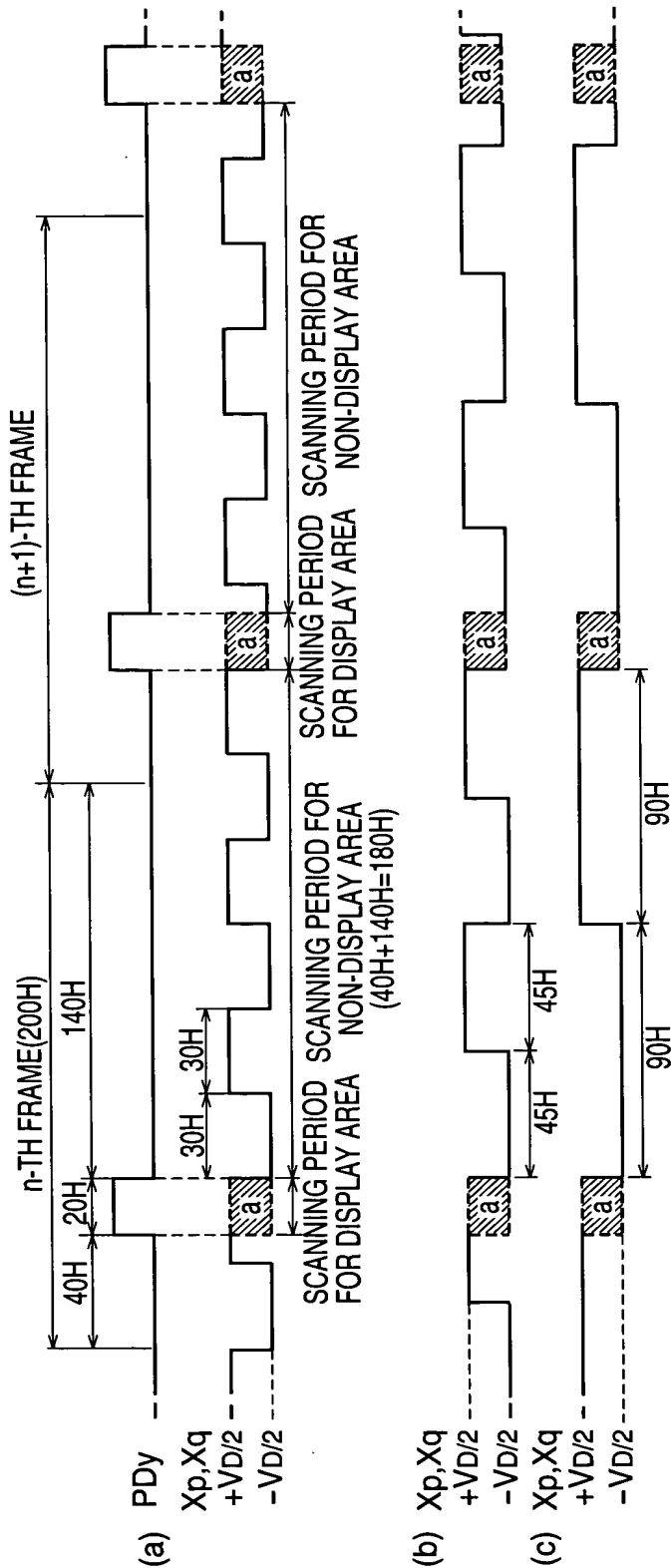
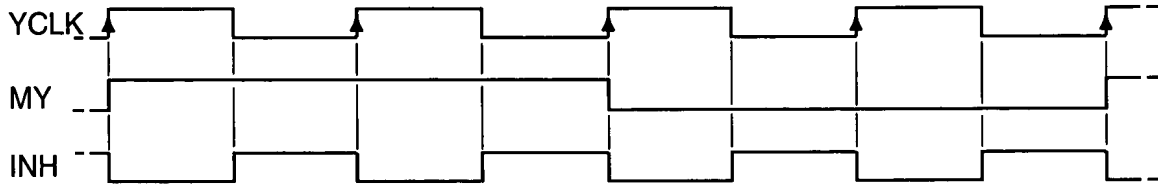


FIG. 11



<Y SIDE>



<X SIDE>

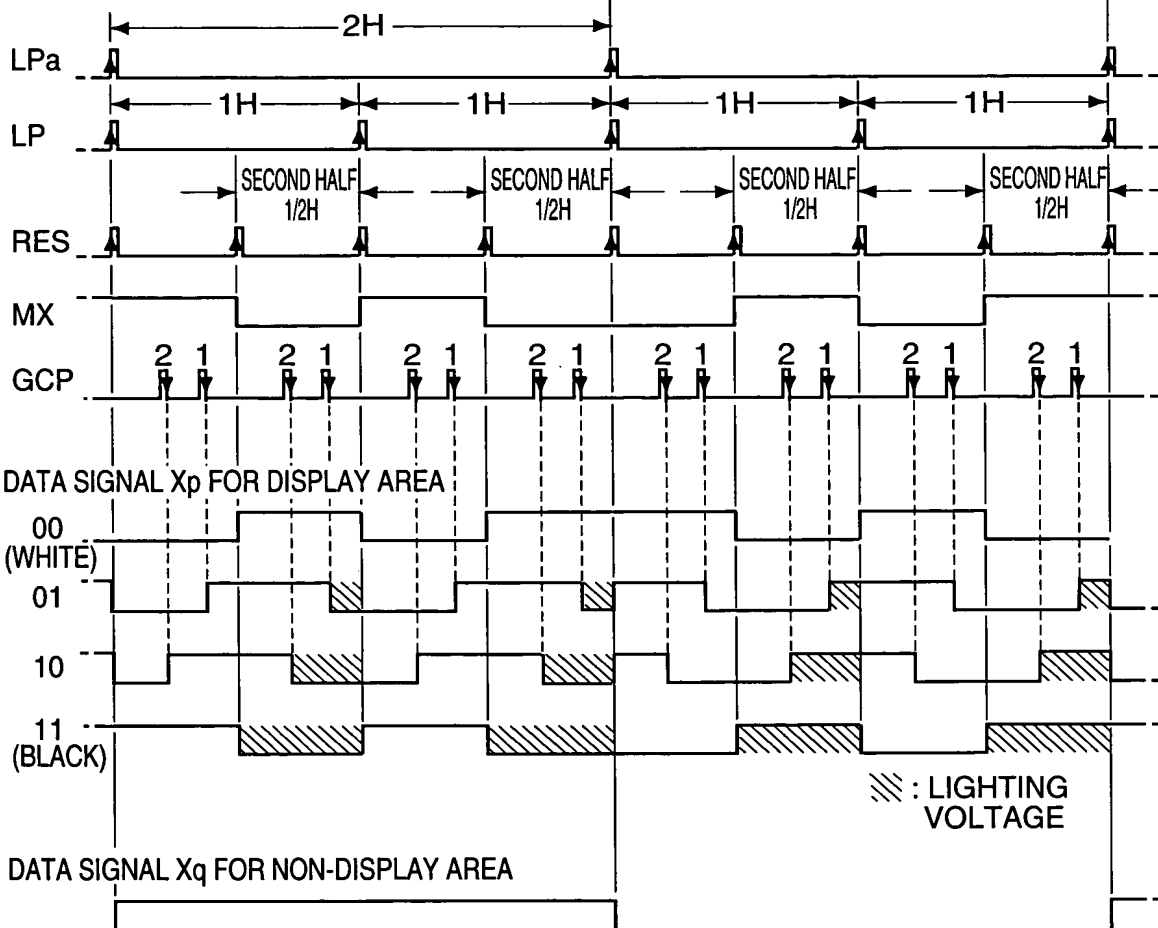


FIG. 12

<1/2H SELECTED, 2H INVERTED>

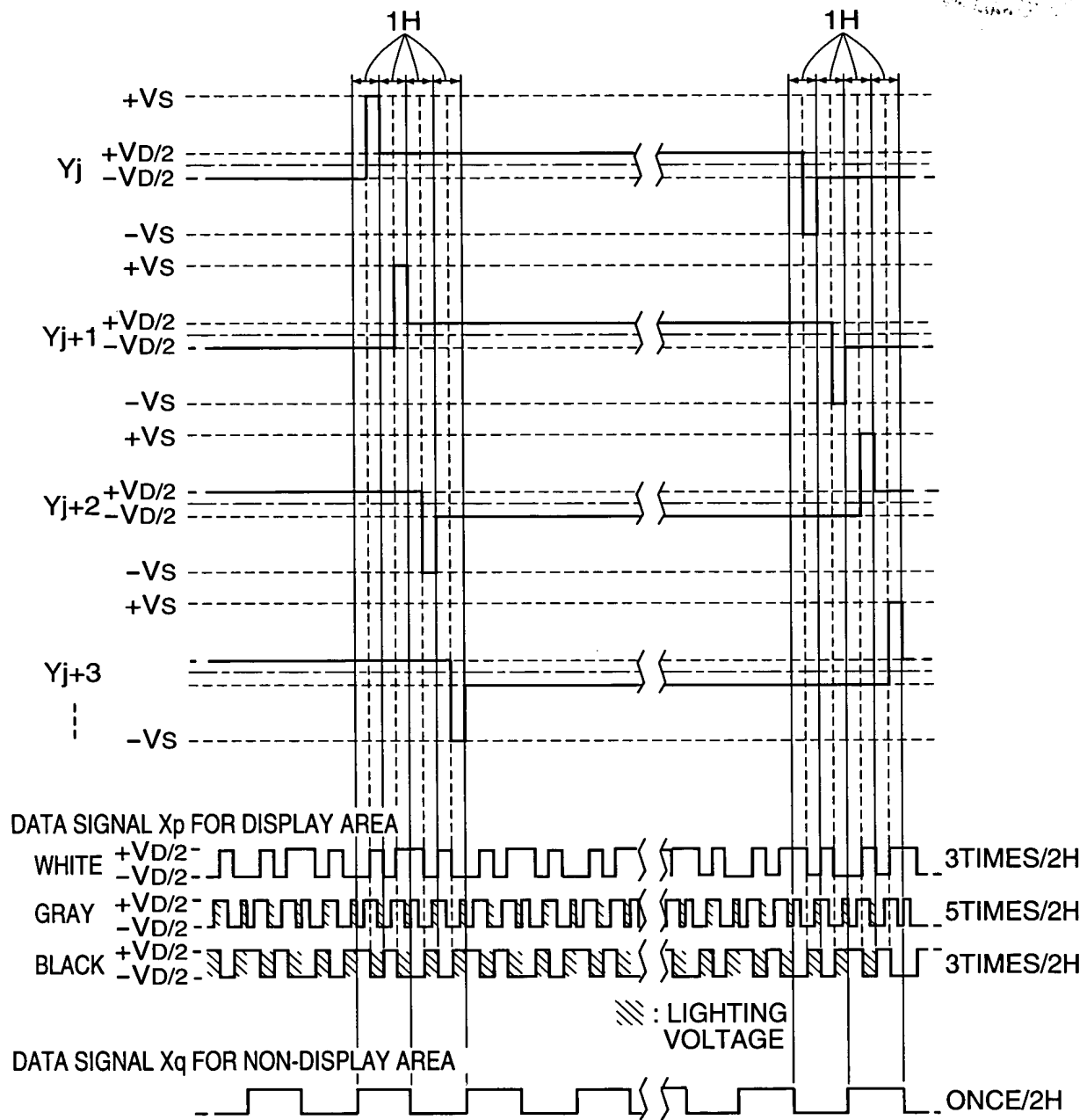


FIG. 13

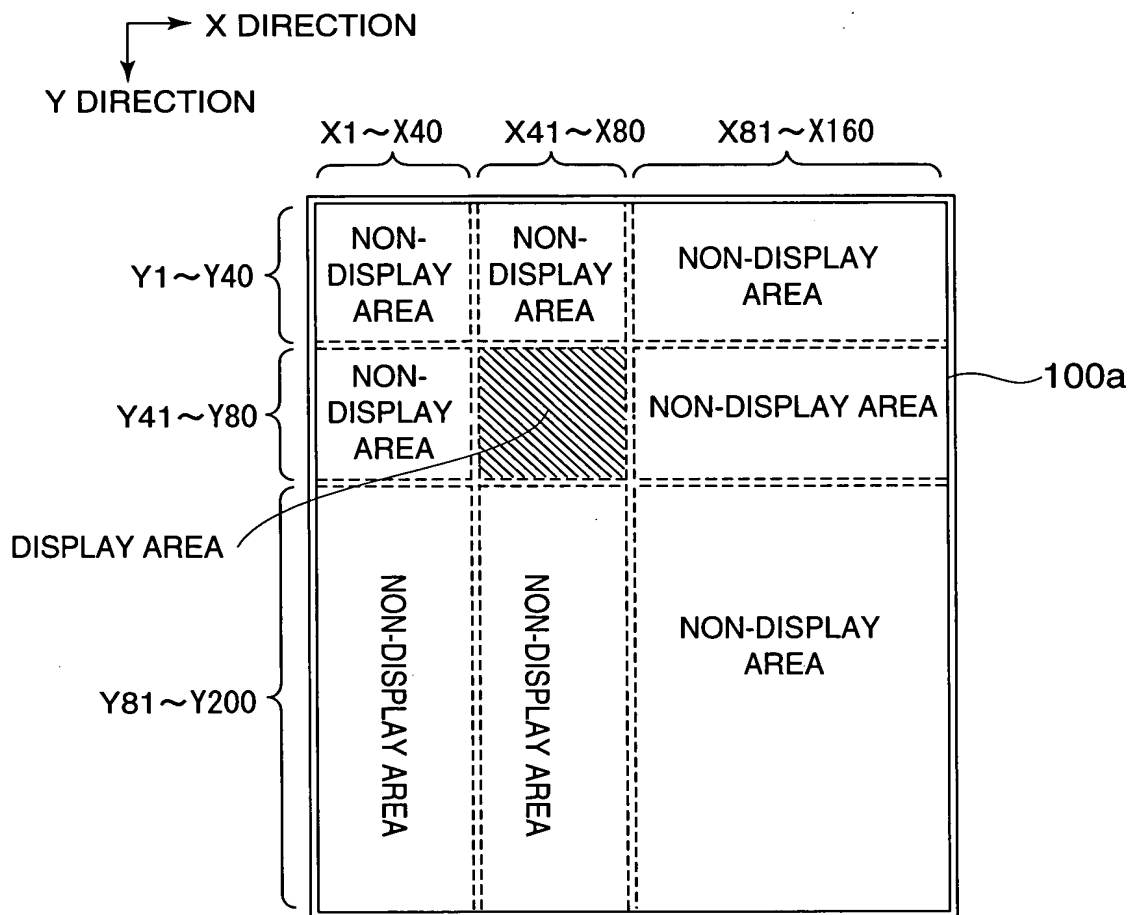
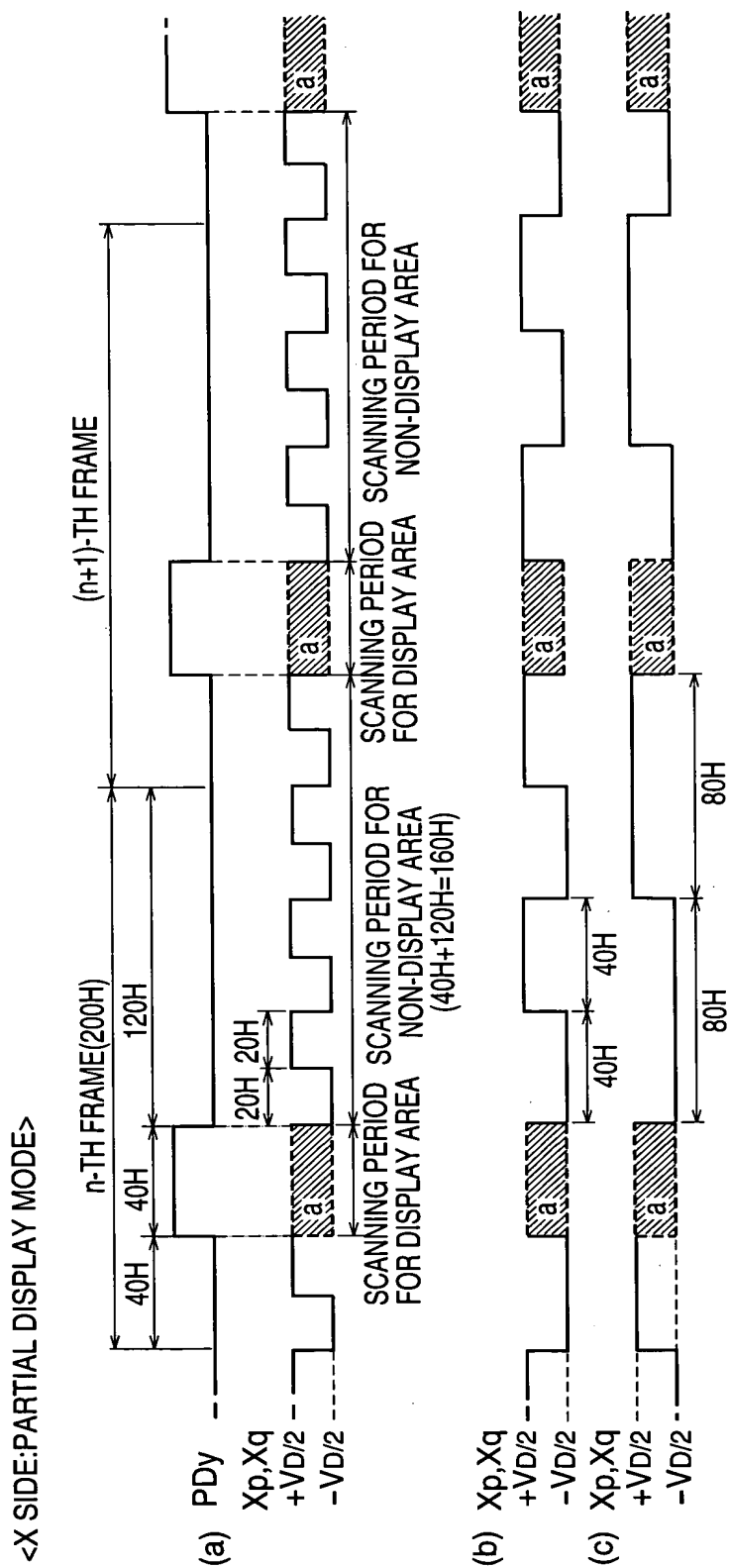


FIG. 14



<1/2H SELECTED, 4H INVERTED>

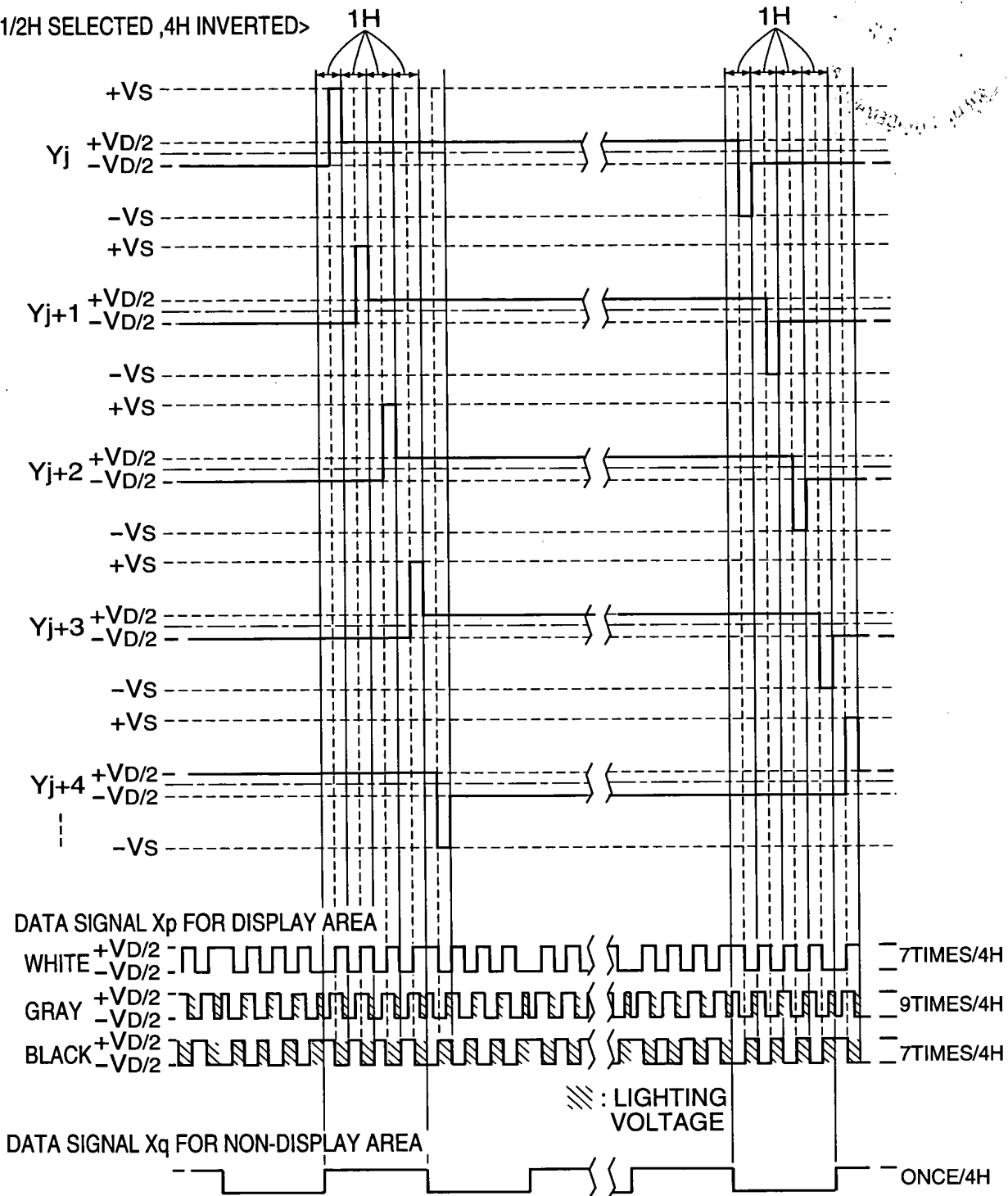


FIG. 16



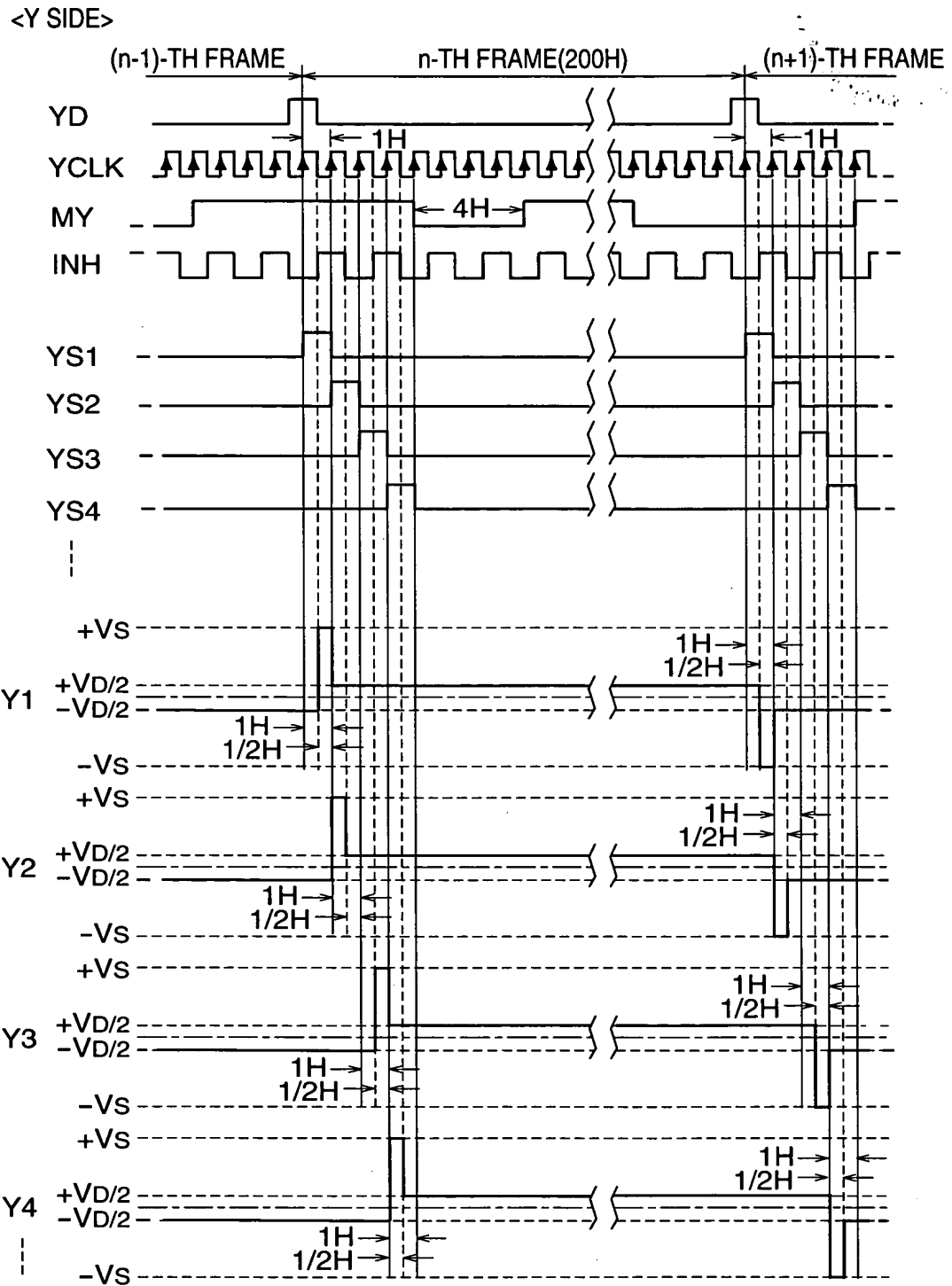


FIG. 17

**<Y SIDE>**

YCLK

MY

INH

<X SIDE>

LPb

LP

RES

MX

GCP

DATA SIGNAL Xp FOR DISPLAY AREA

00  
(WHITE)

01

10

11

(BLACK)

### DATA SIGNAL Xq FOR NON-DISPLAY AREA

/// : LIGHTING  
VOLTAGE

FIG. 18

<1/2H SELECTED ,4H INVERTED,FIRST AND SECOND HALVES >

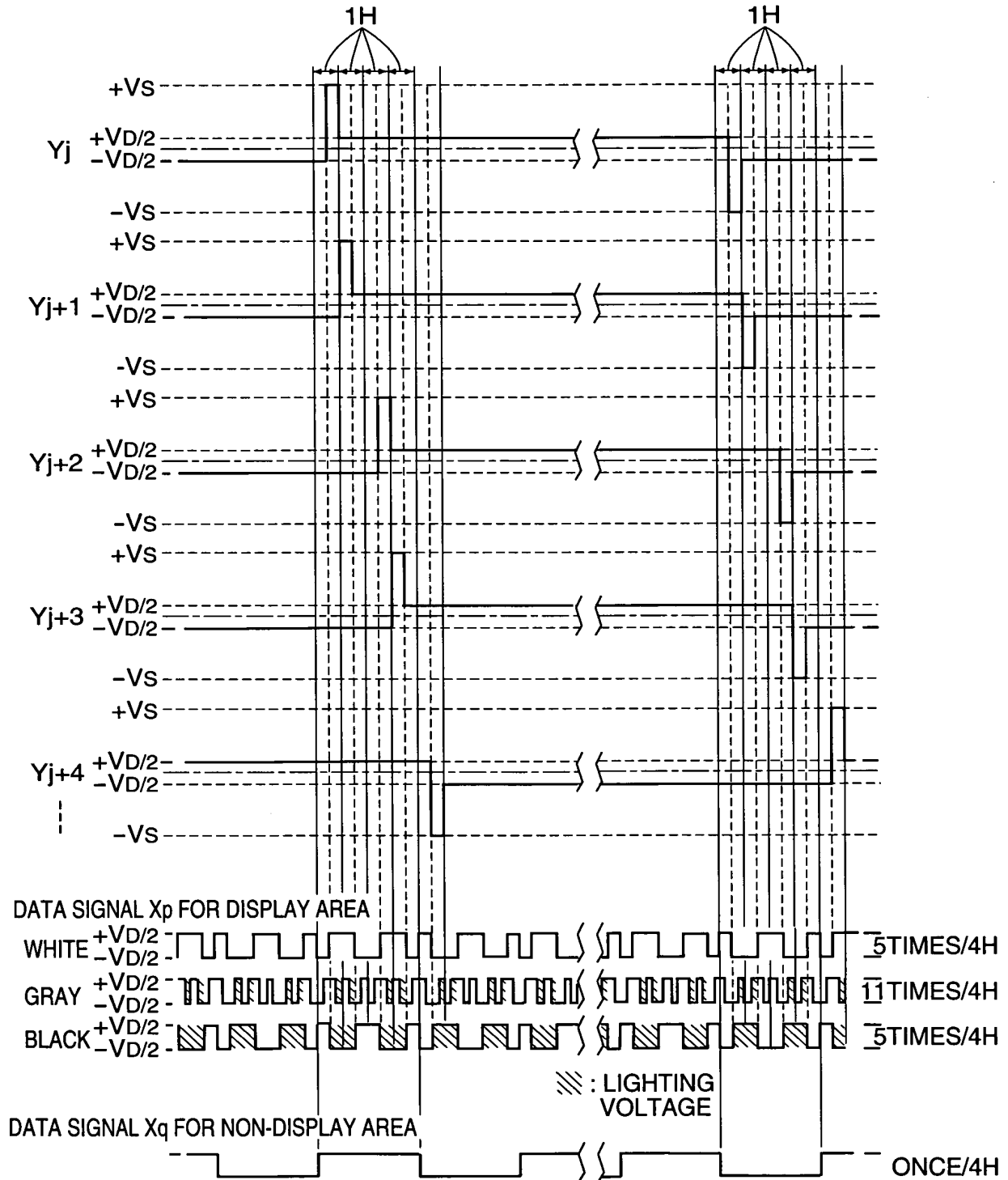
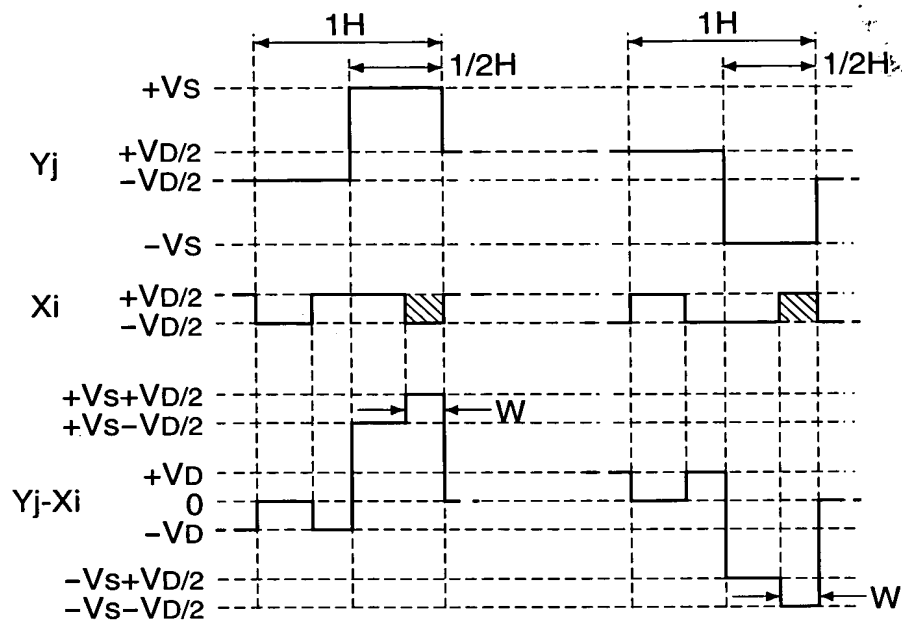
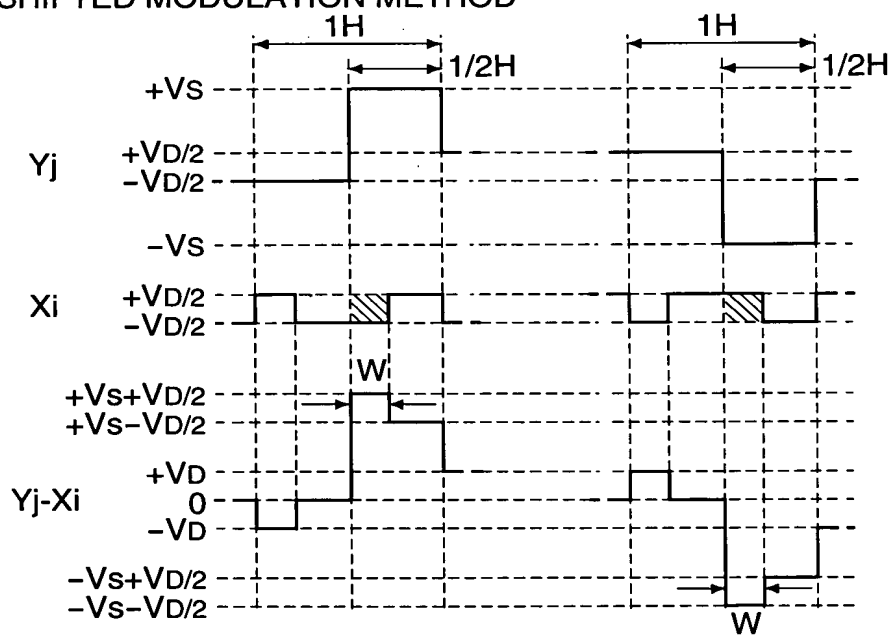


FIG. 19

(a) RIGHT-SHIFTED MODULATION METHOD



(b) LEFT-SHIFTED MODULATION METHOD



/// : LIGHTING VOLTAGE

FIG. 20

**<Y SIDE>**

YCLK

MY

INH

<X SIDE>

LPC

LP

RES

MX

**GCPR**

GCPL

DATA SIGNAL Xp FOR DISPLAY AREA

00

(WHITE)

0-

10

1.

(BLACK)

### DATA SIGNAL Xq FOR NON-DISPLAY AREA

/// : LIGHTING  
VOLTAGE

FIG. 21

<1/2H SELECTED, 4H INVERTED, FIRST AND SECOND HALVES (LEFT-SHIFTED, RIGHT-SHIFTED) >

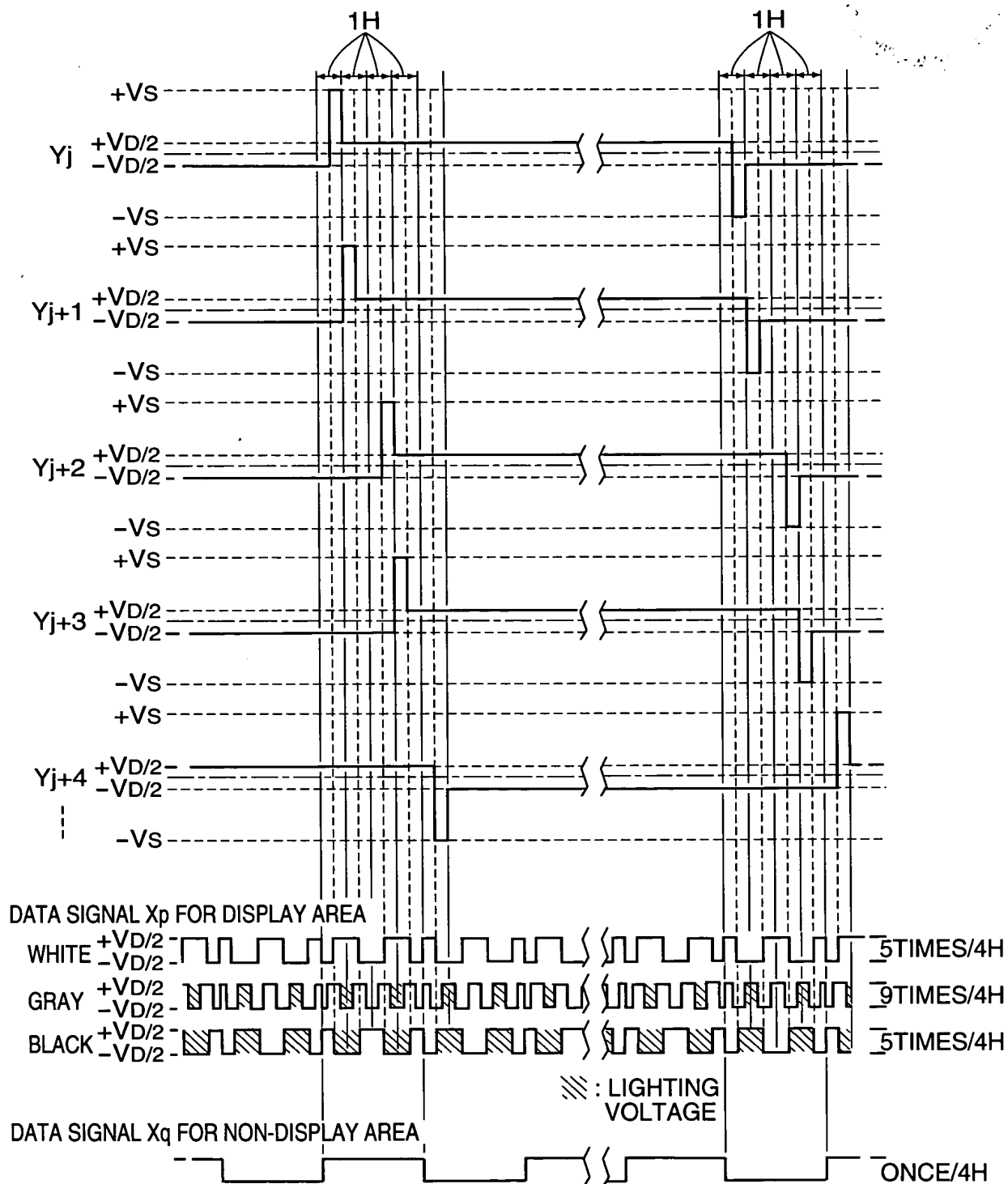


FIG. 22

09021533-11304  
10E1T-235T2260

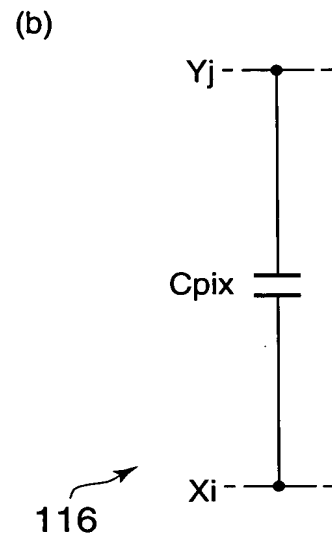
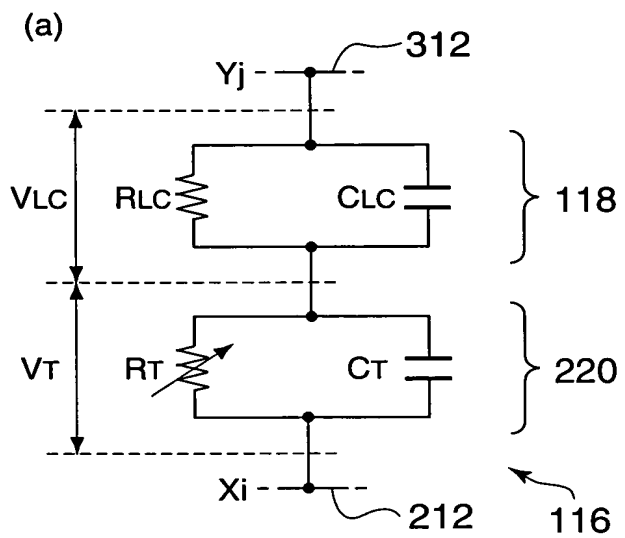


FIG. 23

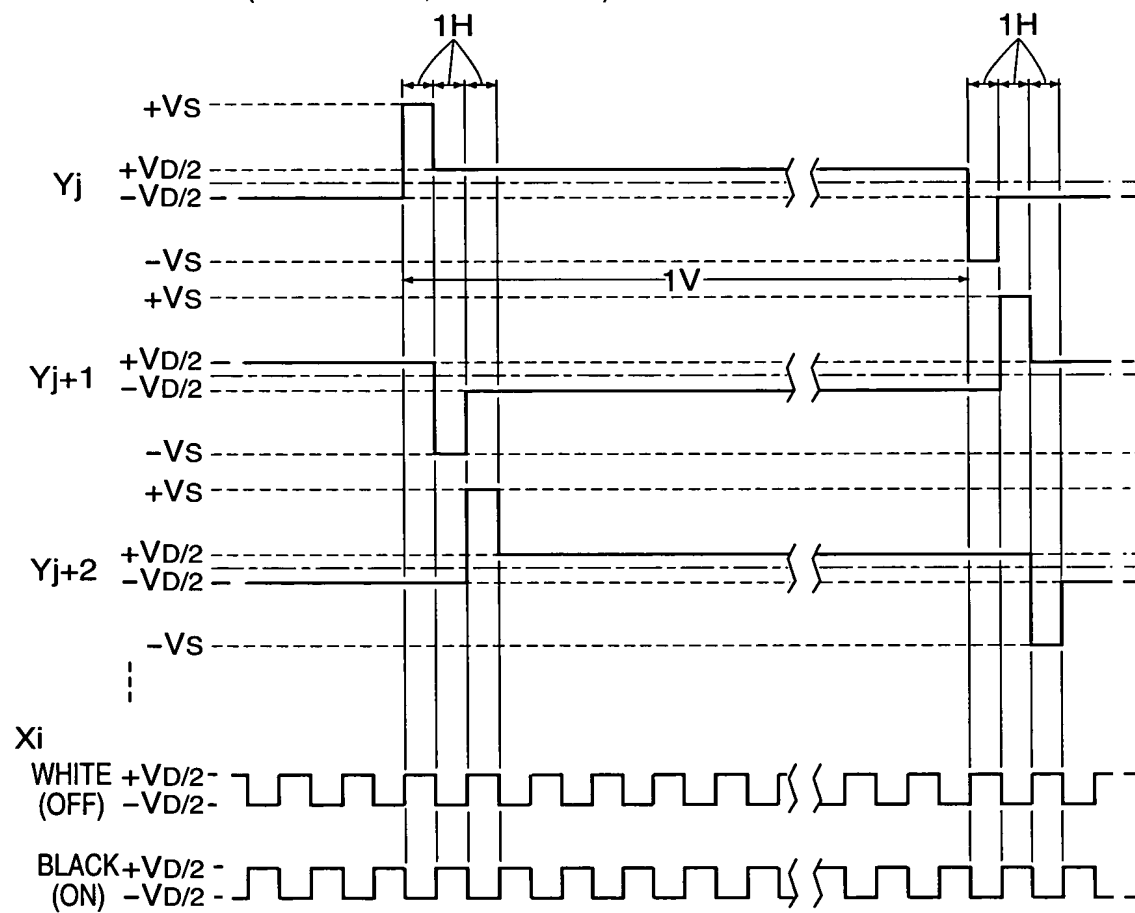


FIG. 24

**SECRET**



FIG. 25

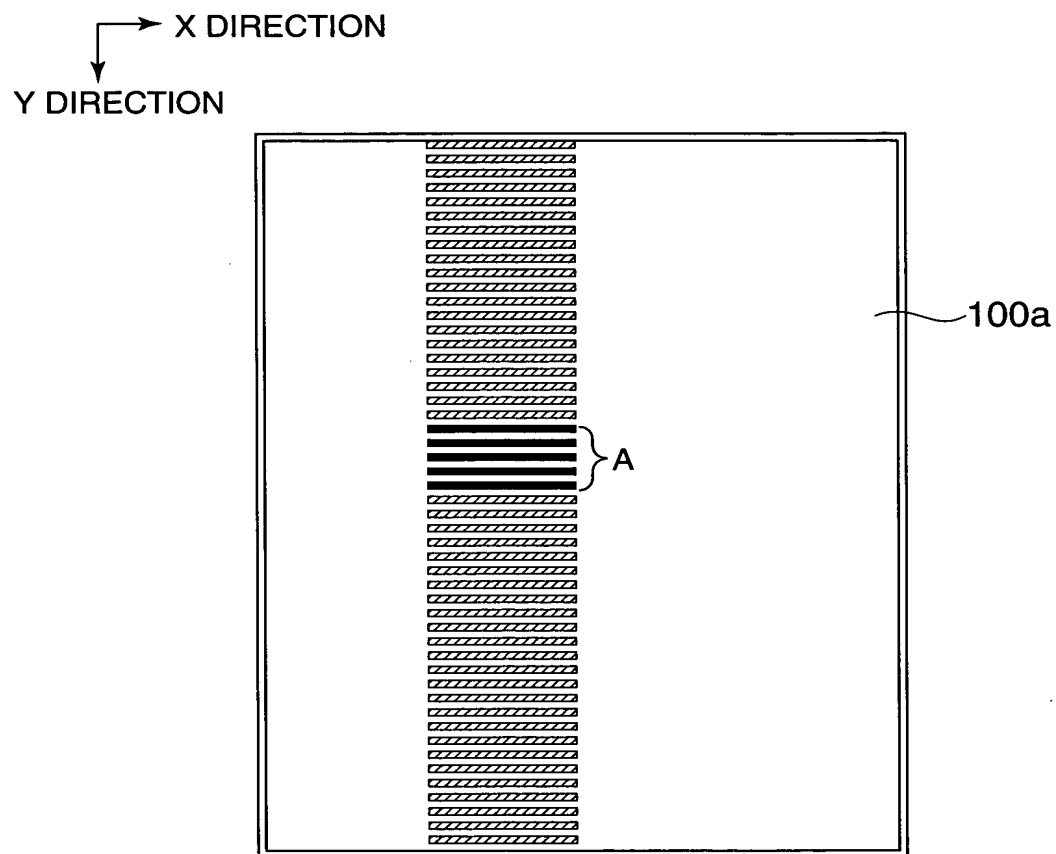


FIG. 25

[illegible]

FIG. 26

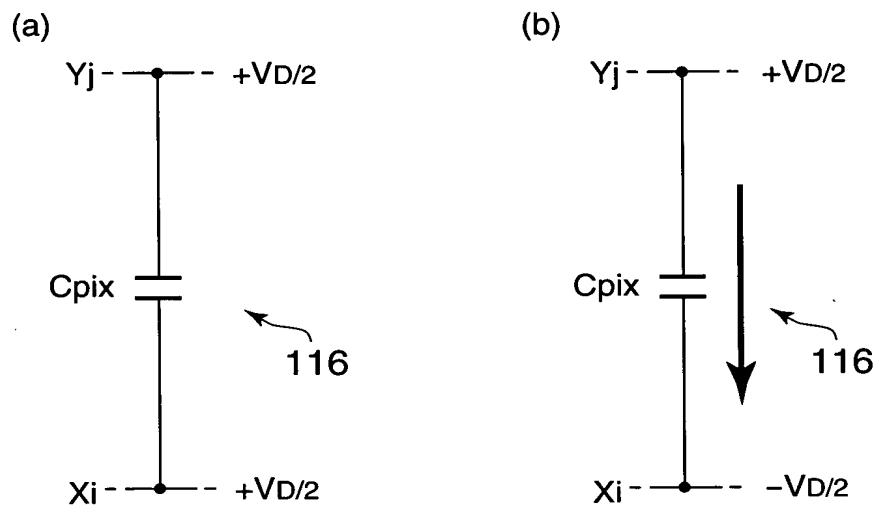


FIG. 27

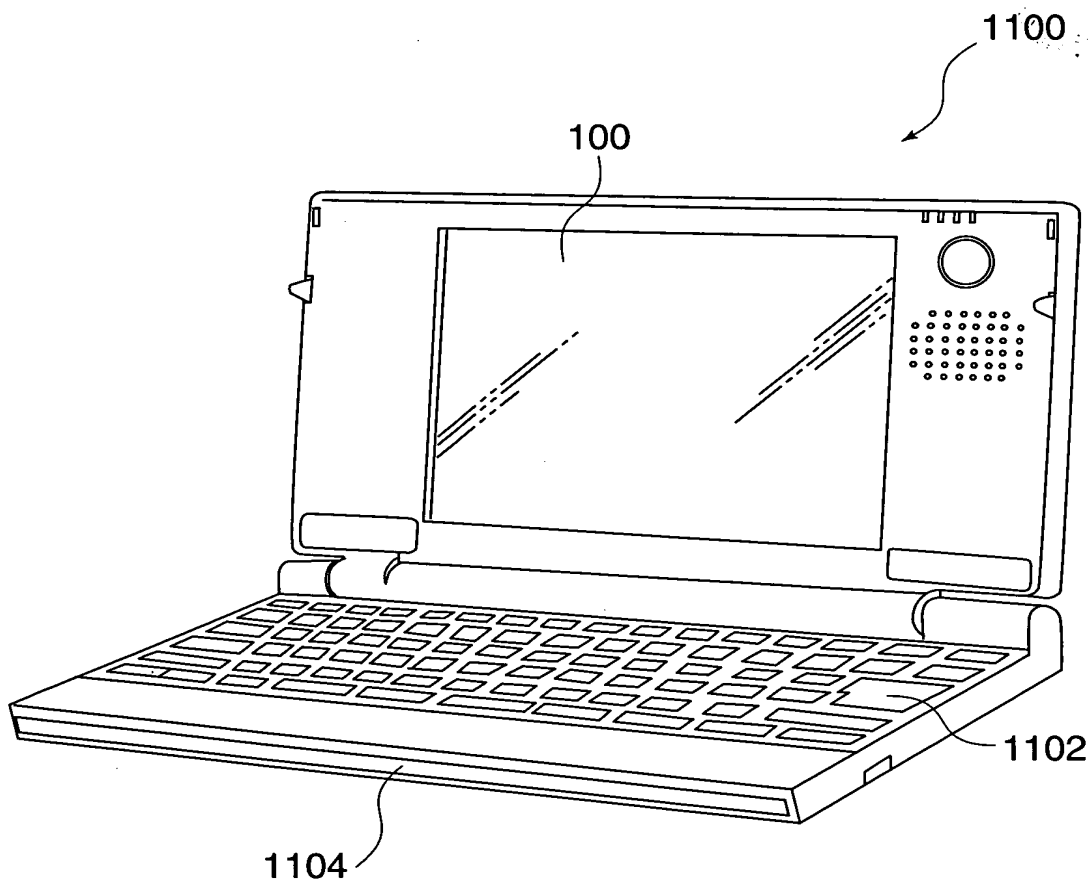


FIG. 28

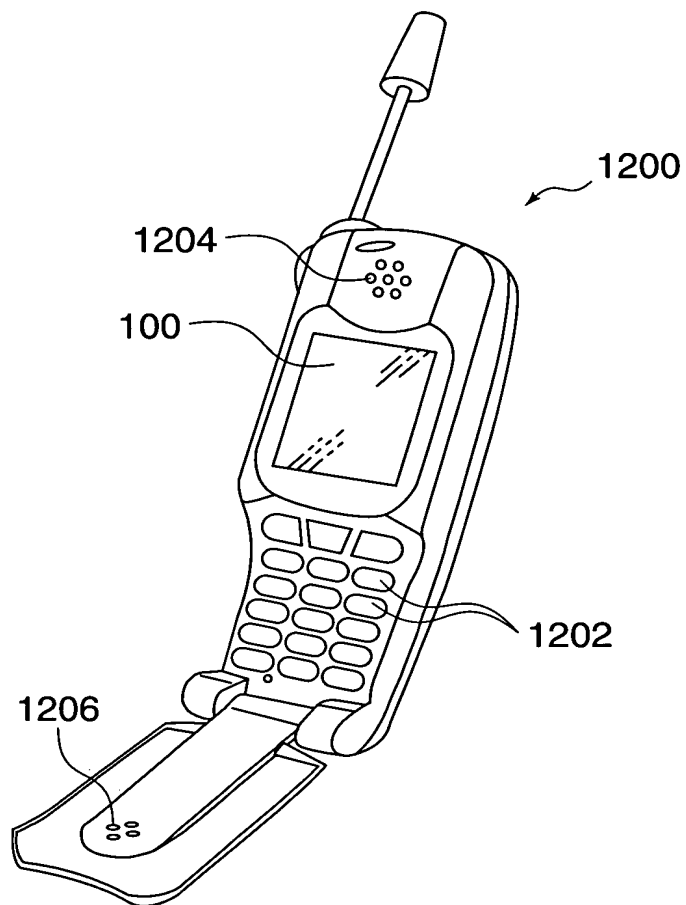


FIG. 29

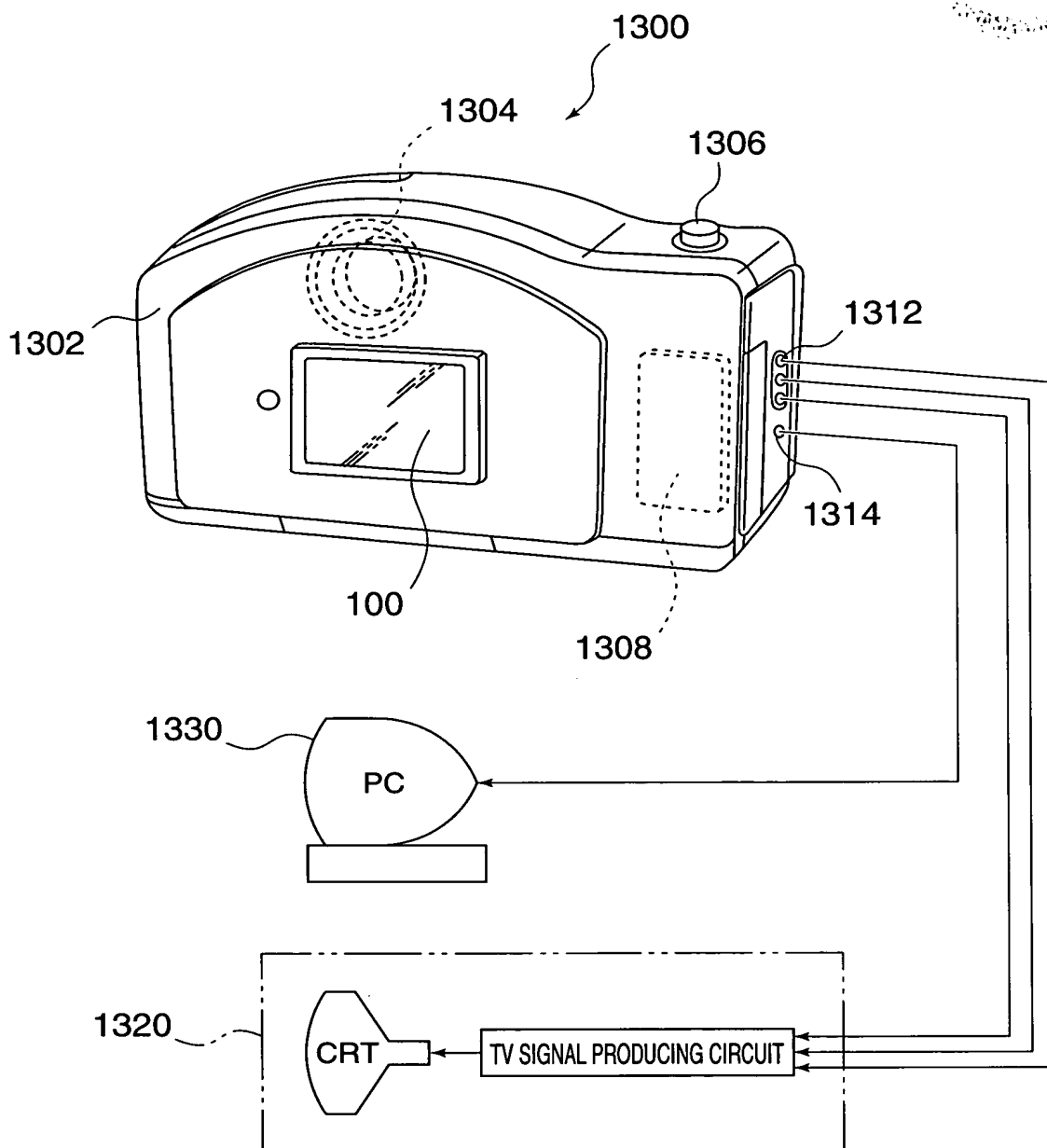


FIG. 30

X DIRECTION  
Y DIRECTION

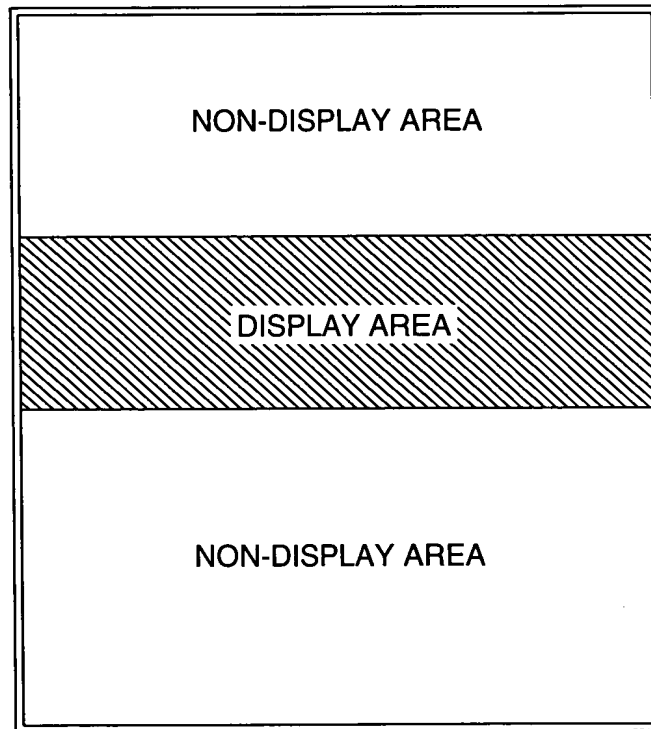


FIG. 31